AMENDMENTS TO SPECIFICATION

- Please replace the paragraph beginning at page 9, line 20 with the following amended paragraph:

On the other hand, when variation in temperature ~~is occurred~~ occurs, the operation of the bias current control circuit 13 in accordance with the present invention can be explained as follows.

- Please replace the paragraph beginning at page 9, line 24 with the following amended paragraph:

Assuming that the reference voltage Vref is an external reference voltage independent of temperature and the bias current control circuit 13 is not considered, then as temperature rises, the voltage Vp at the node [[p]] P will change since the voltage Vp is equal to the two base-emitter voltage drop 2Vbe through transistors Q1 and Q2. In other words, as temperature rises, the base-emitter voltage drop Vbe is reduced and thus Vp decreases. As a result, more current flows through resistor R2 and thus the bias current IB also increases. Inversely, as temperature is lowered, the Vbe increases and thus Vp increases. That is, less current flows through resistor R2 and thus the bias current IB also decreases. Therefore, when temperature rises, the voltage Vp at the node P needs to be increased in order to maintain the bias current IB substantially constant and, if otherwise, the voltage Vp has to be decreased.